

Adrian M. Ionescu

Short bio

Adrian M. Ionescu is an Associate Professor at the Swiss Federal Institute of Technology, Lausanne, Switzerland. He received the B.S./M.S. from the Polytechnic Institute of Bucharest, Romania in 1989 and the Ph.D from the National Polytechnic Institute of Grenoble, France, in 1997. He has held staff and/or visiting positions at LETI-CEA, Grenoble, France, LPCS-ENSERG, Grenoble, France and Stanford University, USA, in 1998 and 1999. His research interests focus on micro- and nano-electronic devices aimed at integrated circuit design - especially process development, modeling and electrical characterization. His research group is working on various topics in the field of silicon micro/nano-electronics with special emphasis on new nanoscale solid-state devices and their applications for new functionality and performance, RF MEMS/NEMS and 3D device integration.

Dr. Ionescu has published more than 100 articles in international journals and conferences. He received three Best Paper Awards in international conferences and the Annual Award of the Technical Section of the Romanian Academy of Sciences in 1994. He served in the ISQED and IEDM conference technical committees in 2003 and 2004 and he was the Technical Program Committee Chair of ESSDERC in 2006.

He is Director of the Laboratory of Micro/Nanoelectronic Devices (LEG-2) and also served as Director of the Institute of Microelectronics and Microsystems of EPFL from 2002 to 2006. He serves as national representative of Switzerland in the ENIAC Mirror Group.